

## **G59/3 TYPE TEST VERIFICATION REPORT**

Type Tested reference number			GW3048-EM, GW3648-EM,GW5048-EM		
System supplier name			Jiangsu GoodWe Power Supply Technology Co.,Ltd.		
Address			NO.189 Kun Lun Jiangsu,china	Shan Road, Suzhou New District,	
Tel	+86 512 6239 7998		Fax	+86 512 6239 7972	
E:mail	service@goodw	e.com.cn	Web site	http://www.goodwe.com.cn	
		3	kW single phase		
Maximum export capacity, use separate sheet if more than one 3.68		kW single phase			
		5	kW single phase		

System supplier declaration.

- I certify on behalf of the company named above as a supplier of a Generating Unit, that all products supplied by the company with the above Type Test reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site modifications are required to ensure that the product meets all the requirements of G59/3.

Signed

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On behalf of



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Power Quality. Harmonics.								
Generating Unit tested to BS EN 61000-3-12								
Generating	er phase	4.6 kVA		Harmonic % = Measured Value				
(rpp)					1	(Amps) x 23/rating per phase (kVA)		
Harmonic	At 45-55% outp	of rated ut	100% of	rate	d output	Limit in BS EN 61000-3-12		
	Measured		Measure	ed	1			
	Value MV in	%	Value M	/ in	%	1 phase	3 phase	
	Amps	1	Amps		1		1/	
2	0.010	0.05%	0.005		0.03%	8%	8%	
3	0.322	1.61%	0.317		1.59%	21.6%	Not stated	
4	0.010	0.05%	0.011		0.06%	4%	4%	
5	0.026	0.13%	0.019		0.10%	10.7%	10.7%	
6	0.003	0.02%	0.003		0.02%	2.67%	2.67%	
7	0.013	0.07%	0.011		0.06%	7.2%	7.2%	
8	0.008	0.04%	0.005		0.03%	2%	2%	
9	0.007	0.04%	0.009		0.05%	3.8%	Not stated	
10	0.008	0.04%	0.006		0.03%	1.6%	1.6%	
11	0.009	0.05%	0.009		0.05%	3.1%	3.1%	
12	0.005	0.03%	0.006		0.03%	1.33%	1.33%	
13	0.006	0.03%	0.005		0.03%	2%	2%	
THD	0.362	1.81%	0.264		1.32%	23%	13%	
PWHD	0.548	2.74%	0.538		2.69%	23%	22%	

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**Power Quality.** Voltage fluctuations and Flicker. The tests should be carried out on a single Generating Unit. Results should be normalised to a standard source impedance or if this results in figures above the limits set in BS EN 61000-3-11 to a suitable Maximum Impedance.

		Starting		1	Stopping		Run	ning
	d max	d c	d(t)	d max	d c	d(t)	P st	P lt 2
	1			1				hours
		-		1			1	
Measured Values at	12			1			1	
test impedance	1							
							1	
Normalised to	0.58%	0.31%	0.00%	0.85%	0.39%	0.30%	0.06	0.09
standard impedance	0.5070	0.5170	0.0070	0.0370	0.3370	0.5070	0.00	0.05
		1			-			
Normalised to	1-			1			1	
required maximum		-	-		-	-		-
impedance	1						1	
Limits set under BS EN	4%	3.3%	3.3%	4%	3.3%	3.3%	1.0	0.65
61000-3-11	1	-						
Test Impedance	R		-	Ω	XI	-	-//	Ω
	/	-		//			//	
Standard Impedance	R	0	).4	Ω	XI		0.25	Ω
	/		-	/		-	/	
Maximum Impedance	R		-	Ω	XI	-		Ω

Power quality. DC injection			
Test power level	10%	55%	100%
Recorded value in Amps	0.041	0.036	0.018
as % of rated AC current	0.21%	0.18%	0.09%
Limit	0.25%	0.25%	0.25%



**Power Quality.** Power factor. The tests should be carried out on a single Generating Unit. Test are to be carried out at three voltage levels and at full output. Voltage to be maintained within + or -1.5% of the stated level during the test.

	216.2V	230V	253V	Measured at three voltage levels and at
		1		full output Voltage to be maintained
Measured value	0.998	0.999	0.999	within + or $-1.5\%$ of the stated level
Limit	>0.95	>0.95	>0.95	during the test.

## **Protection. Frequency tests**

	< / /			/		/	
Function	Set	Setting		test	"No-trip tests"		
	Fraguanay	Time delay	Fraguanay	Time delay	Frequency	Confirm no	
	riequency	Hine delay	riequency	Time delay	/time	trip	
					51 3Hz		
O/F stage 1	51.5Hz	90s	51.52Hz	90.4 s	51.5112	no trip	
	$\langle \rangle$				95s		
					<b>51 0U</b>		
O/F stage 2	52Hz	0.5s	52.01 Hz	872 ms	51.882	no trip	
					89.98s/90.24	ep	
					52.2Hz		
					0.48s/892ms/	no trip	
	$\geq$				902ms		
		2					
		20	47 4011	20.40	47.7Hz		
U/F stage 1	47.5Hz	20s	47.49Hz	20.19s	255	no trip	
					255		
			1		47.2Hz		
U/F stage 2	47Hz	0.5s	47.00Hz	740.74 ms	10 00- /20 22-	no trip	
					19.985/20.235		
	< >				46.8 Hz		
						no trip	
					0.48s/829ms/	no trip	
				1	840ms		



Protection. Voltage tests							
Function	Setti	ng	Tri	o test	"No trip-test	"No trip-tests" All phases at	
					same	voltage	
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip	
O/V stage 1	262.2V	1.0s	262.21V	1.42 s	258.2V 2.0 sec	no trip	
					260 71/260 7		
O/V stage 2	273.7V	0.5s	270.66 V	692.52 ms	3	no trip	
					0.98s/1.456s		
					277.7V/277.7		
	$\geq$				2	no trip	
				1	0.48s/960ms		
					204.1V		
U/V stage 1	200.1V	2.55	198.79 V	2.95 s	3.5s	no trip	
11/V stage 2	1941/	0.50	197 26 V	040ms	188V/188.03	no trin	
0/V stage 2	1041	0.55	182.50 V	9401115	2.48s/2.92s	no trip	
				1	180v/180.05		
				1	0.48	no trip	
	3			1	sec/948ms	1	

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Protection. Loss o	f Mains test	and single pha	ase test.			
Test Power and	33%	66%	100%	33%	66%	100%
imbalance	-5% Q	-5% Q	-5% P	+5% Q	+5% Q	+5% P
Trip time. Limit is 0.5s	340ms	330ms	276ms	260ms	258ms	298ms



Protection. Frequency change, Stability test									
	Start Frequency	Change	End Frequency	Confirm no trip					
Positive Vector Shift	49.5Hz	+9 degrees		no trip					
Negative Vector Shift	50.5Hz	- 9 degrees		no trip					
Positive Frequency drift	49.5Hz	+0.19Hzs <sup>-1</sup>	51.5Hz	no trip					
Negative Frequency drift	50.5Hz	-0.19Hzs <sup>-1</sup>	47.5Hz	no trip					

**Protection. Re-connection timer**. The tests should prove that the reconnection sequence starts in no less than 20s for restoration of voltage and frequency to within the stage 1 settings of table 10.5.7.1

Test should prove that the reconnection sequence starts in no less than 20s for restoration of voltage and frequency to within the stage 1 settings of table 10.5.7.1

Time delay	Measured	Checks on no reconnection when voltage or frequency is brought to just							
setting (s)	delay (s)	outside stage 1 limits of table 10.5.7.1.							
	1								
20s	46.4s	At 266.2V	At 196.1V	At 47.4Hz	At 51.6Hz				
Confirmation	that the				11				
Generating U	Init does	no reconnection	no reconnection	no reconnection	no reconnection				
not re-conne	nect								



Fault level contribution.							
		/					
For machines with electro-magnetic out	put	/	For Inverter	output			
		/		< <i>/</i>			
Parameter	Symbol	Value	Time after	Volts	Amns		
Turumeter	Symbol	value	Time ditter	Volta	Amps		
		1	fault				
		1					
Peak Short Circuit current	İn	/	20ms	-7.9V	1.2A		
	P	1		<hr/>			
Initial Value of aperiodic current	Δ		100mc	7.9\/	200m4		
initial value of aperiodic current	4		1001115	-7.0V	-200111A		
Initial symmetrical short-circuit	$I_k$		2E0mc	7 51/	200m4		
current*		/	2501115	-7.5V	-20011A		
		/					
symmetrical (aperiodic) component of	inc			$\langle \rangle$			
symmetrical (apendule) component of	'DC		500ms	-7.5V	-400mA		
short circuit current*							
		1					
Reactance/Resistance Ratio of	X/R		Time to				
*				63.	6ms		
source*			trip				

Self Monitoring solid state switching	NA
It has been verified that in the event of the solid state switching device failing to	
disconnect the Generating Unit, the voltage on the output side of the switching device is	
reduced to a value below 50 Volts within 0.5 seconds	

Additional comments

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GW4248D-ES is similar to GW5048D-ES in circuit and construction except for output rating of current and power. The test result can refer to GW5048D-ES.